



2014 NASA/ESA Conference on Adaptive Hardware and Systems (AHS-2014)



JPL



July 14-17, 2014

University of Leicester,
Leicester, UK



ADEVO

Organized by

National Aeronautics and Space Administration – Jet Propulsion Laboratory (NASA-JPL)
European Space Agency (ESA), Netherlands
Fraunhofer Institute for Integrated Circuits IIS, Germany
University of Leicester, UK

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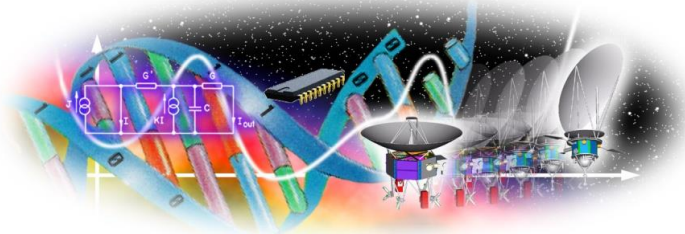
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The 2014 NASA/ESA Conference on Adaptive Hardware and Systems (AHS-2014) will be held during July 14 - 17, 2014, at the University of Leicester, United Kingdom. As in previous editions, AHS-2014 will seek technical sponsorship from IEEE Circuits and Systems Society (IEEE-CAS) and in-Cooperation status with ACM Special Interest Group on Design Automation (ACM-SIGDA).

The purpose of the conference is to bring together leading researchers from the adaptive hardware and systems community to exchange experiences and share new ideas in the field. The conference expands the topics addressed by the precursor series of NASA/DoD Conference on Evolvable Hardware, held between 1999 and 2005, with a broader scope. This includes a variety of hardware and system adaptation methods and targeting more industry participation. The NASA/ESA series started with the AHS-2006 conference held in Istanbul, Turkey, and continued annually with the AHS-2007 conference held in Edinburgh, UK, the AHS-2008 conference held at ESTEC, in Noordwijk, The Netherlands, the AHS-2009 conference held in San Francisco, USA, AHS-2010 conference held in Anaheim, USA, AHS-2011 conference held in San Diego, USA, the AHS-2012 conference held in Erlangen, Germany and the AHS-2013 conference held at Politecnico di Torino, Torino, Italy.

Adaptation reflects the capability of a system to maintain or improve its performance in the context of internal or external changes, such as uncertainties and variations during fabrication, faults and degradations, modifications in the operational environment, incidental or intentional interference, different users and preferences, modifications of standards and requirements, trade-offs between performance and resources.

We welcome original contributions in the areas of hardware and software adaptation at different system levels, including, but not limited to, tools and algorithms for adaptive system design (e.g. adaptation-aware compilers), novel applications of adaptive hardware and systems (e.g. intelligent agent machines), and enabling technologies for such systems (e.g. instrumentation platforms, reconfigurable and multi-core architectures). We also welcome novel contributions in the areas of adaptive transmission for telecommunications (e.g. aware of power limitations, changing environment, and interferences), data compression techniques (e.g. new image compression techniques for space applications), software/hardware architectures for unmanned autonomous vehicles (e.g. adapting to extreme environments and mission unknowns), etc.

While the focus of this conference is on communications and space applications, we welcome original contributions in other application areas such as consumer, medical, defence and security, as the techniques employed can be disseminated across the board. We also welcome papers describing significant applications of adaptive hardware and systems to industrial case studies and demonstrating their capabilities and effectiveness.

In view of the above, the topics to be covered in this conference include, but are not limited to:

Adaptive Hardware

Adaptive antennas
Adaptive embedded systems
Adaptive circuits and configurable IP cores
Adaptive MEMS/NEMS devices
Adaptive optics
Hardware for adaptive signal processing
MEMS/NEMS energy scavenging devices
Emerging technologies-nanoelectronics
Embryonic hardware, morphogenesis
Evolvable, reconfigurable and morphable hardware
Built-in tuneable structures and automated tuning
Built-in self-test and self-repair

Adaptation Algorithms

Learning and evolutionary algorithms for adaptive hardware
Search and optimization algorithms for adaptive hardware
On-chip learning and adaptation
Automatic/self-calibration
Hardware implementations of optimization engines
Adaptive image and data compression

Design Methodology

Algorithms for exploring design space of adaptive hardware
Design for adaptive systems

Reconfigurable Computing

Adaptive computing and run-time reconfiguration
Reconfigurable computing incl. multi-core architectures

Adaptive Applications

Adaptive sensing
Adaptive wired and wireless networks
Adaptive medical and prosthetic devices
Adaptive flight hardware
Adaptive control circuits and adaptive flight hardware
Adaptive wireless for space
Reconfigurable/Adaptive Computing - Supercomputing
Reconfigurable/Adaptive Computing - DSP and Communications
Reconfigurable/Adaptive Computing - Security and Cryptography

Others

Adaptive interfaces
Adaptation with hardware in the loop
Adaptive hardware/software for autonomous systems
Machine Learning

Prospective authors are invited to submit the electronic version of their full paper (i.e. PS, PDF, MSWord) on the [conference web site](http://www.le.ac.uk/ahs2014). Papers are limited to 8 pages and should be submitted in single-spaced, double column, 10 point type on a 8.5" X 11" or equivalent paper with 1" margins on all sides. Each submission should contain the following items: (1) title of paper, (2) author name(s), (3) first author physical address, (4) first author e-mail address, (5) first author phone number, (6) a maximum 200 words abstract (7) the text of the paper, and (8) references. Accepted papers will be published in the conference proceedings and submitted to **IEEE Xplore**.

For further information please check the conference web site <http://www.le.ac.uk/ahs2014>, or contact:

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Paper submission deadline: February 17, 2014
Author notification: May 19, 2014
Camera ready manuscript deadline: June 2, 2014